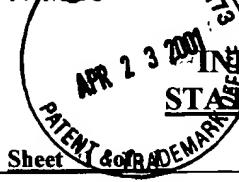


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Application No.	: 09/816,034
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FOREIGN PATENT DOCUMENTS

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		11-41241		JP	Hitachi Ltd.	02/12/1999	
		11-112527 Appl No. 2959540		JP	NEC Corp.	04/23/1999	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

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		“A Generalized Processor Sharing Approach to Flow Control in Integrated Service Networks: The Single-Node Case”, June 1993, A.K. Parekh et al, IEEE/ACM Trans. on Networking, Vol. 1, pp. 344-357.	
		“A Self-Clocked Fair Queueing Scheme for Broadband Applications”, 1994, S. Golestani, pp. 636-646.	
		“Virtual Clock: A New Traffic Control Algorithm for Packet Switching Networks”, 1990, L. Zhang, pp. 19-29.	

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